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# Redundant Array of Inexpensive Interactive Disks (RAI<sup>2</sup>D)

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#### Architecture

We propose a novel protocol that can leverage the properties of a broadcast fabric in order to provide an inexpensive and controller-less RAID<sup>[3]</sup> solution. The proposed architecture is shown below in Figure 1.

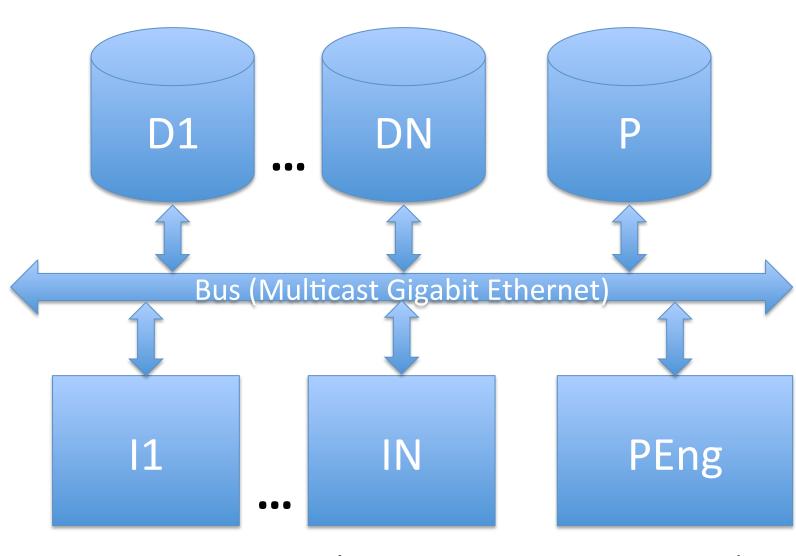


Figure 1: System Architecture – D1-DN represent data disks, P is the parity disk, PEng is the parity engine, and I1-IN are the initiators

A broadcast fabric allows each component actively to snoop on all traffic and be aware of the system state. This allows some logic and decision making to be moved to the components themselves.

### Protocol

Consists of four message types: Control, Data, Status, and Sync. These support three operations: Retrieve, Store, and Sync. As implemented, the array is RAID4.

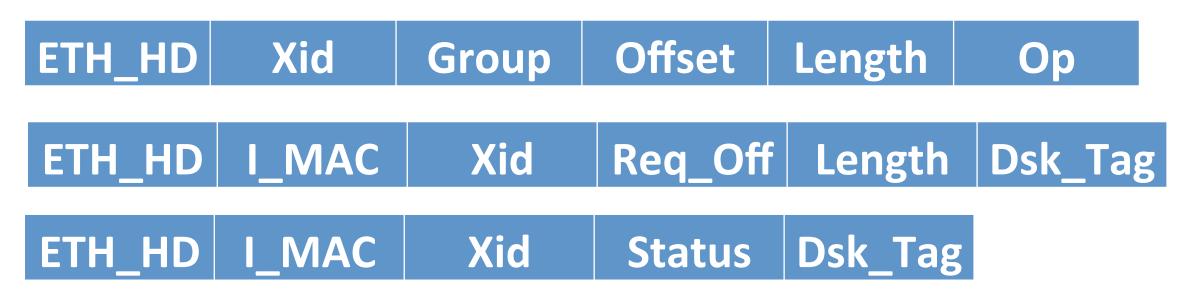
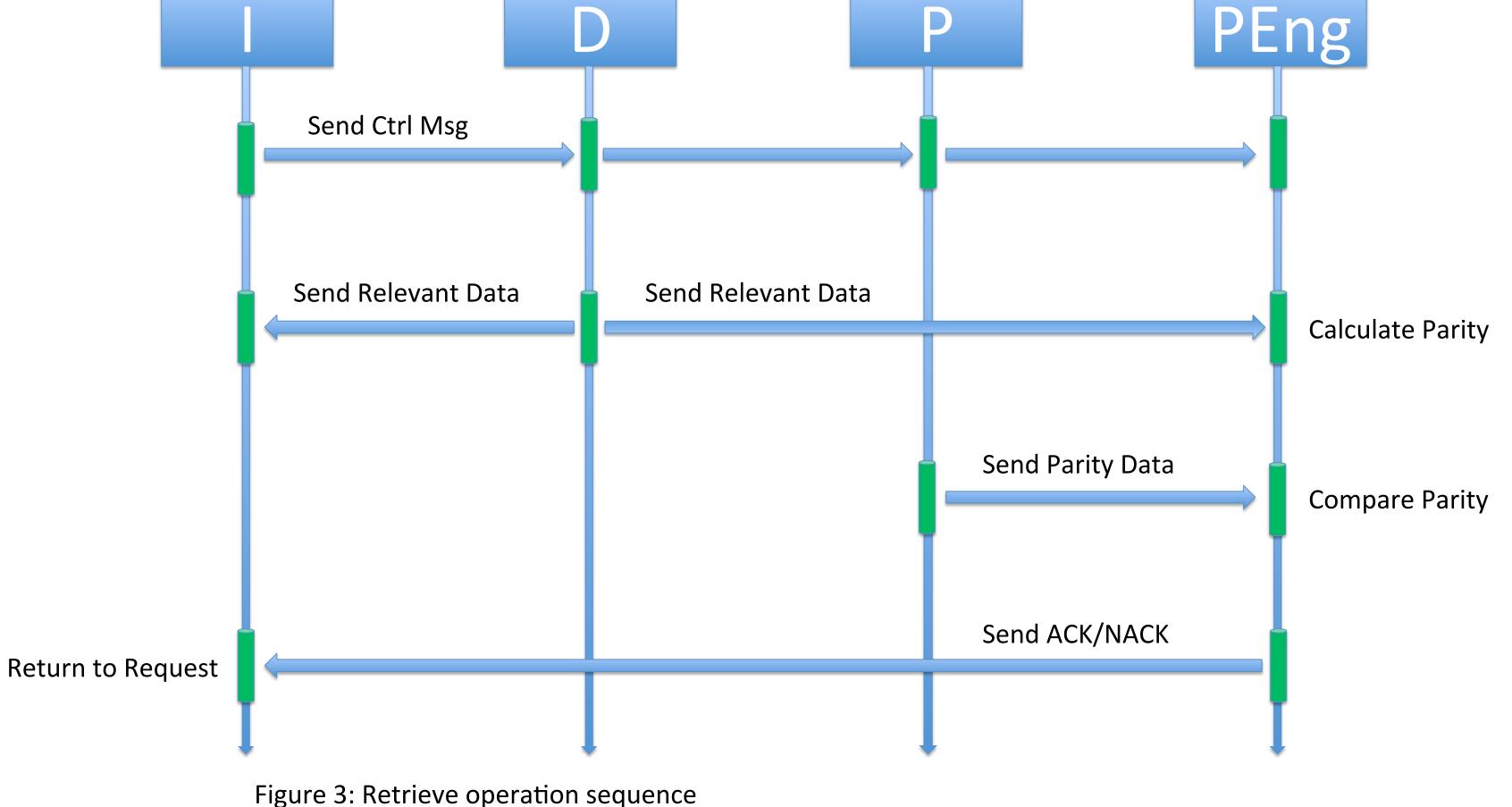
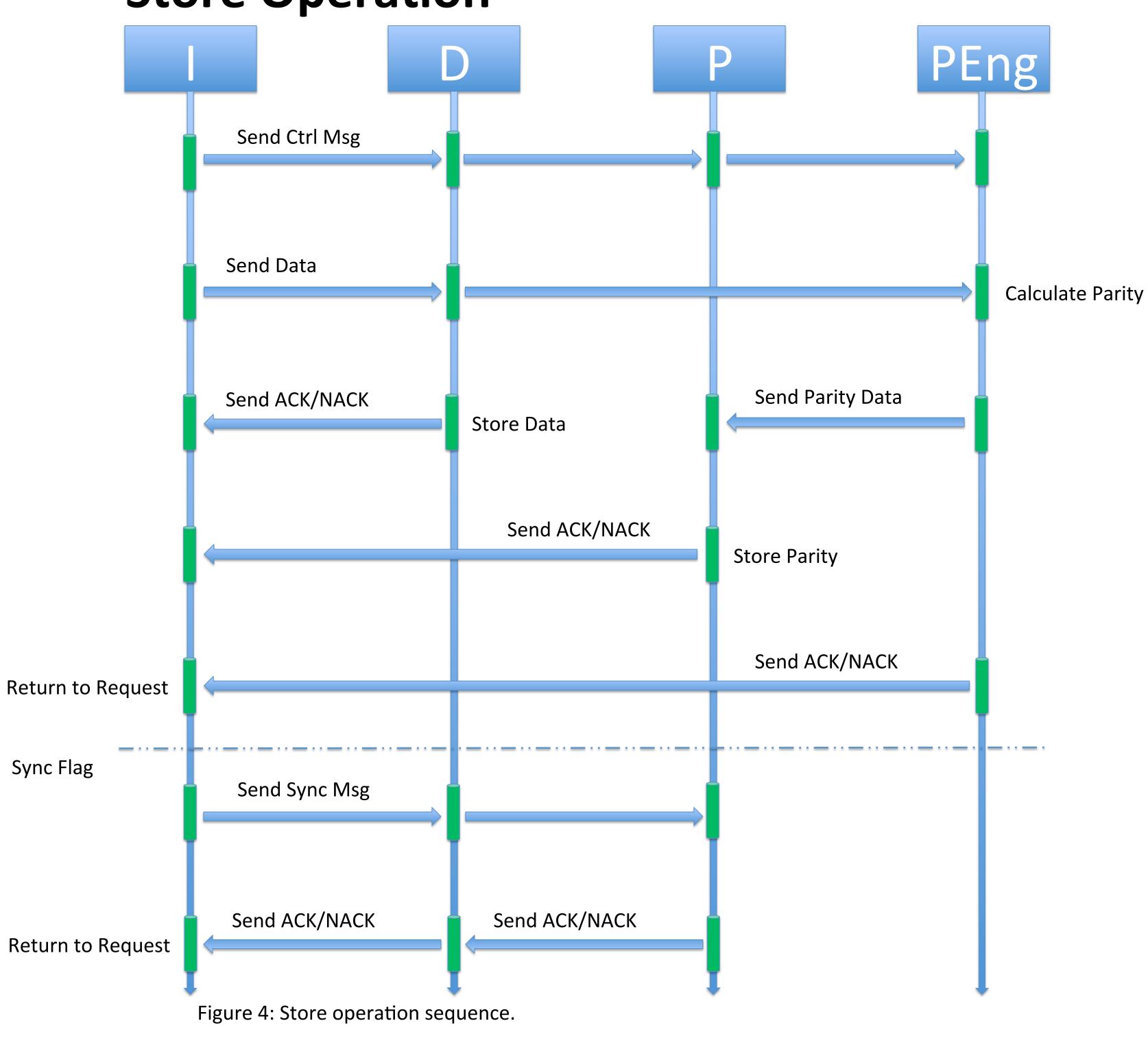


Figure 2: Content of Control, Data, and Status Messages respectively. ETH\_HD is the Ethernet Frame header, I\_MAC is the MAC address of the Initiator and xid is the transaction id.

# Retrieve Operation Liping Retrieve Operation







#### Motivation

Construct a viable alternative RAID architecture that eliminates the RAID controller in favor of a consensus architecture. This would provide a more flexible system. Additionally we want to facilitate easy checking of parity on retrieve operations.

## Throughput

Measured with test: write one GB of data, read one GB of data, timing each separately. Test configuration: two data disks and a single parity disk.

		Write (MB/S)	Read (MB/S)
No Parity	No Sync	94.18	69.66
No Parity	Sync	63.21	71.61
Parity	No Sync	65.64	50.69
Parity	Sync	48.08	50.95

Figure 5: Throughput results

#### **Conclusions and Future Work**

Making state transparent to the system's components and moving decision logic to the components allows for a controller-less RAID solution. Our prototype only scratched the surface of what is possible. Next, exploration of resiliency, atomicity of operations, scalability and other fabrics as well as a kernel block driver for accessibility.

#### References

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